

FIG. 1

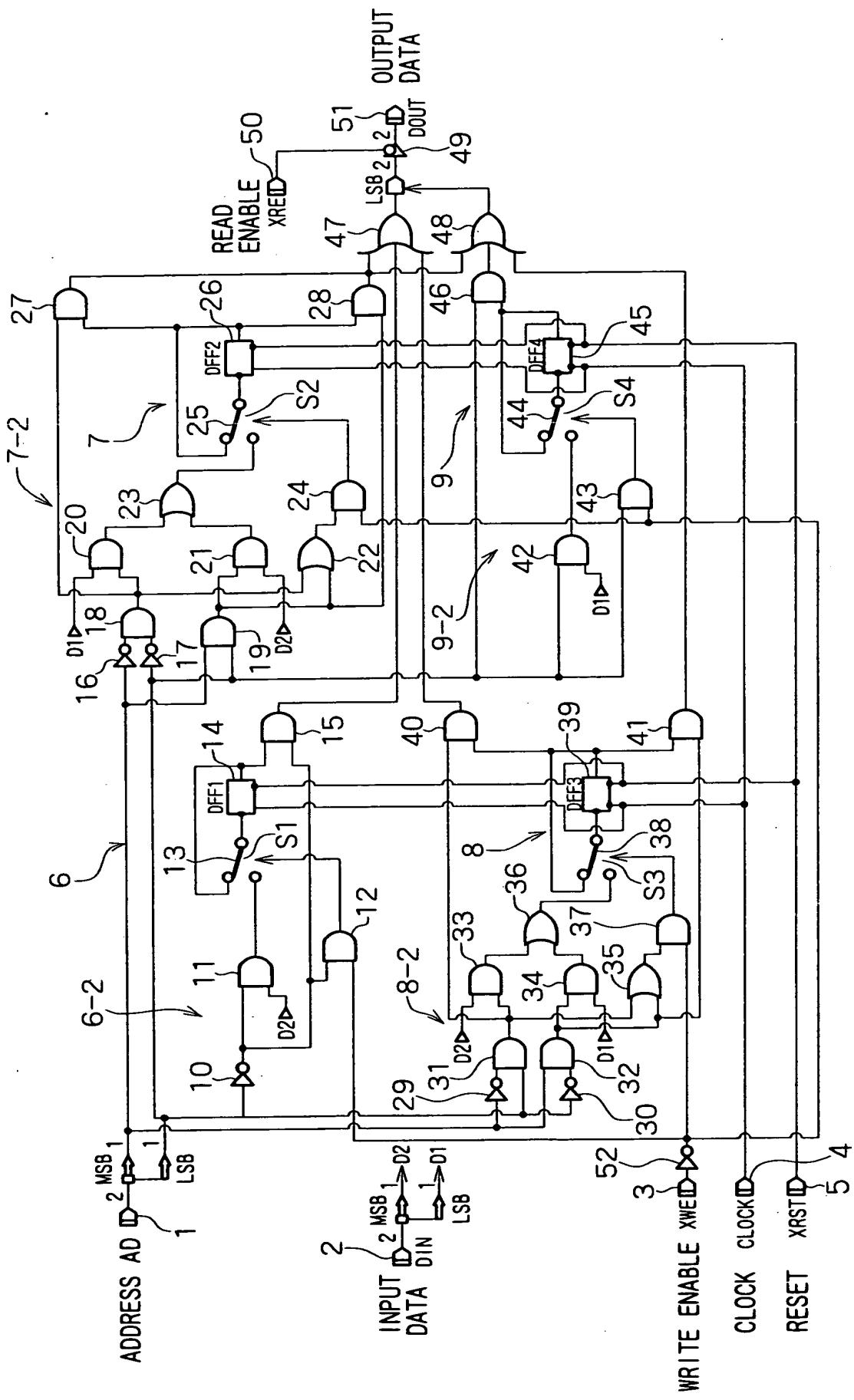


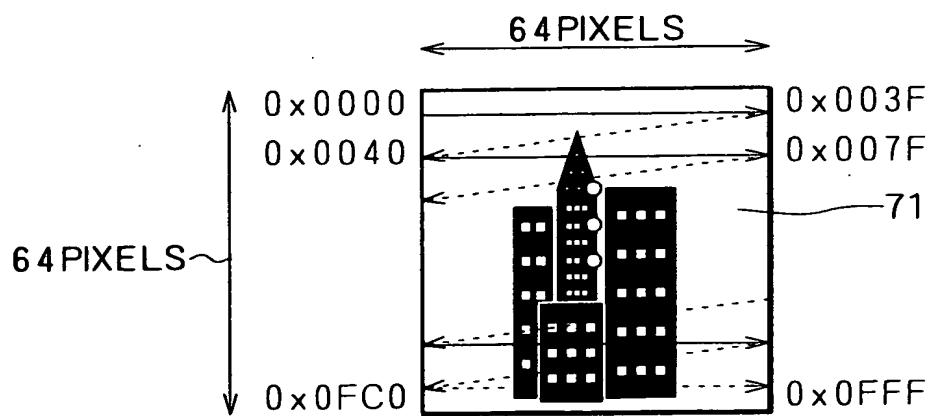
FIG. 2

Diagram illustrating a 2x2 grid of memory locations. The grid is labeled with row and column identifiers:

	61	62
"00" (MSB / LSB)	1	2
"01" (MSB / LSB)	3	4

Arrows point from labels "61" and "62" to the first and second columns respectively.

FIG. 3



TOET2D "42450660

FIG. 4

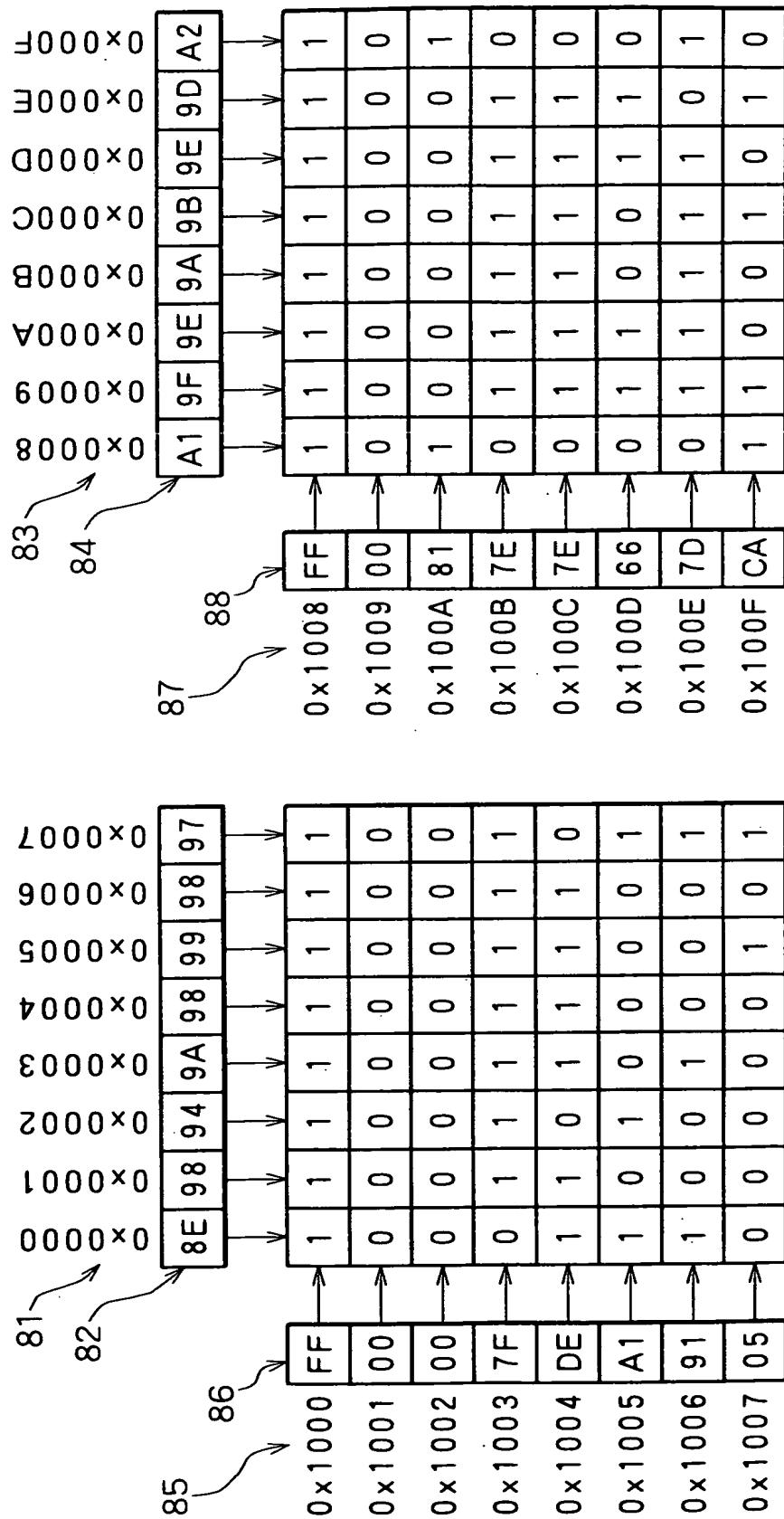


FIG.5

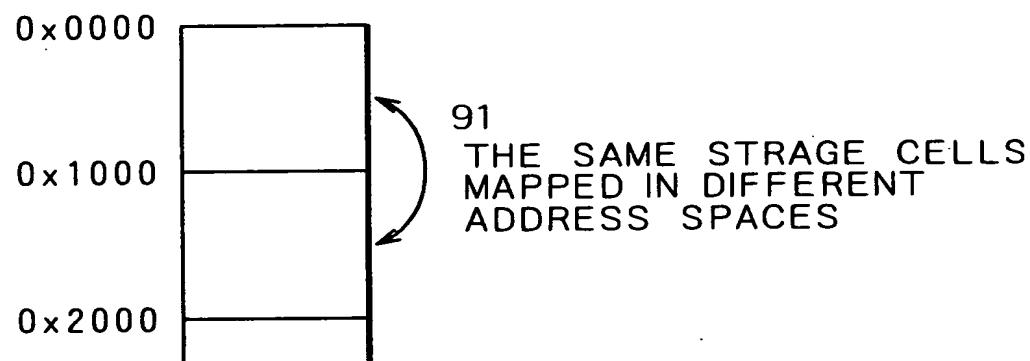
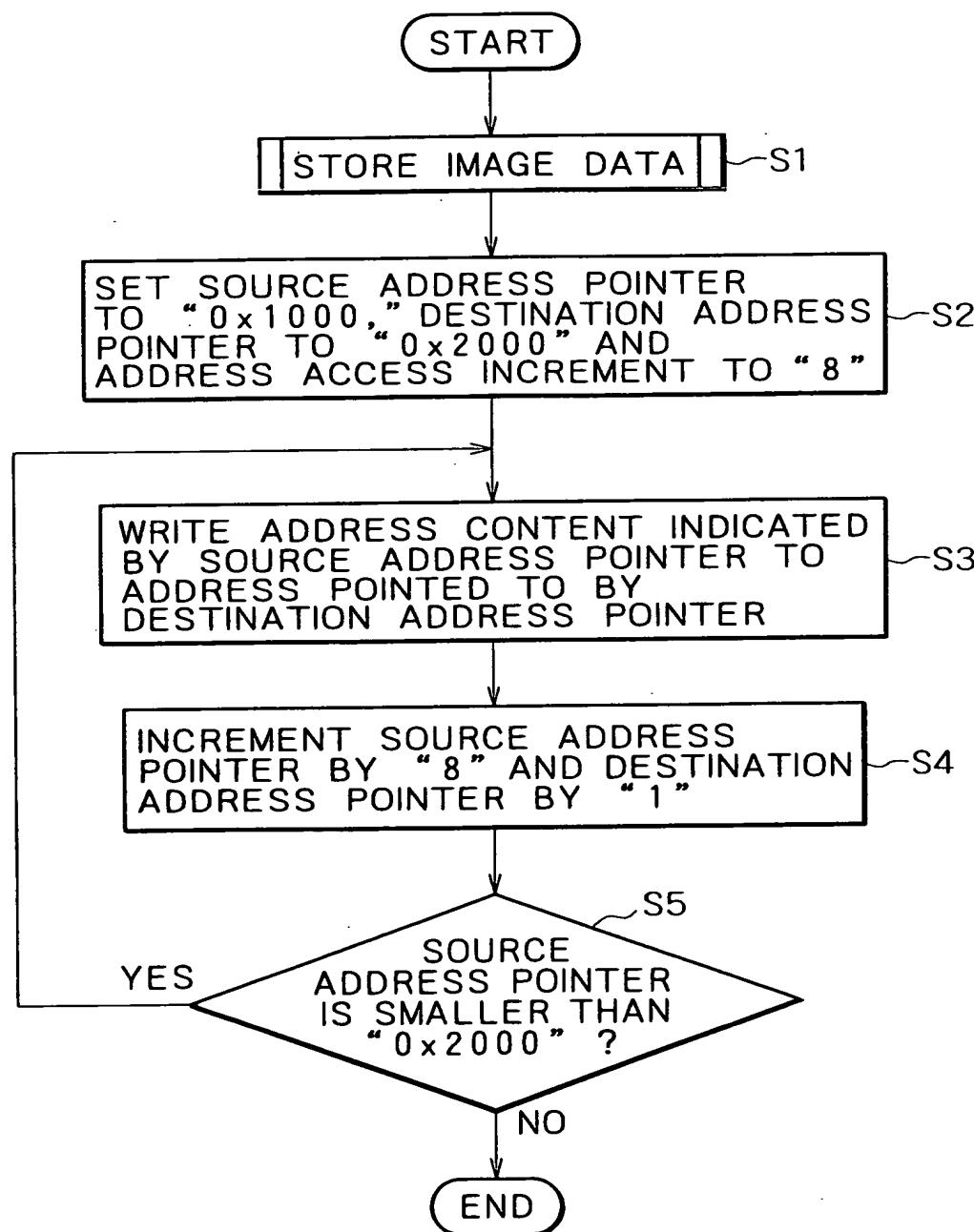
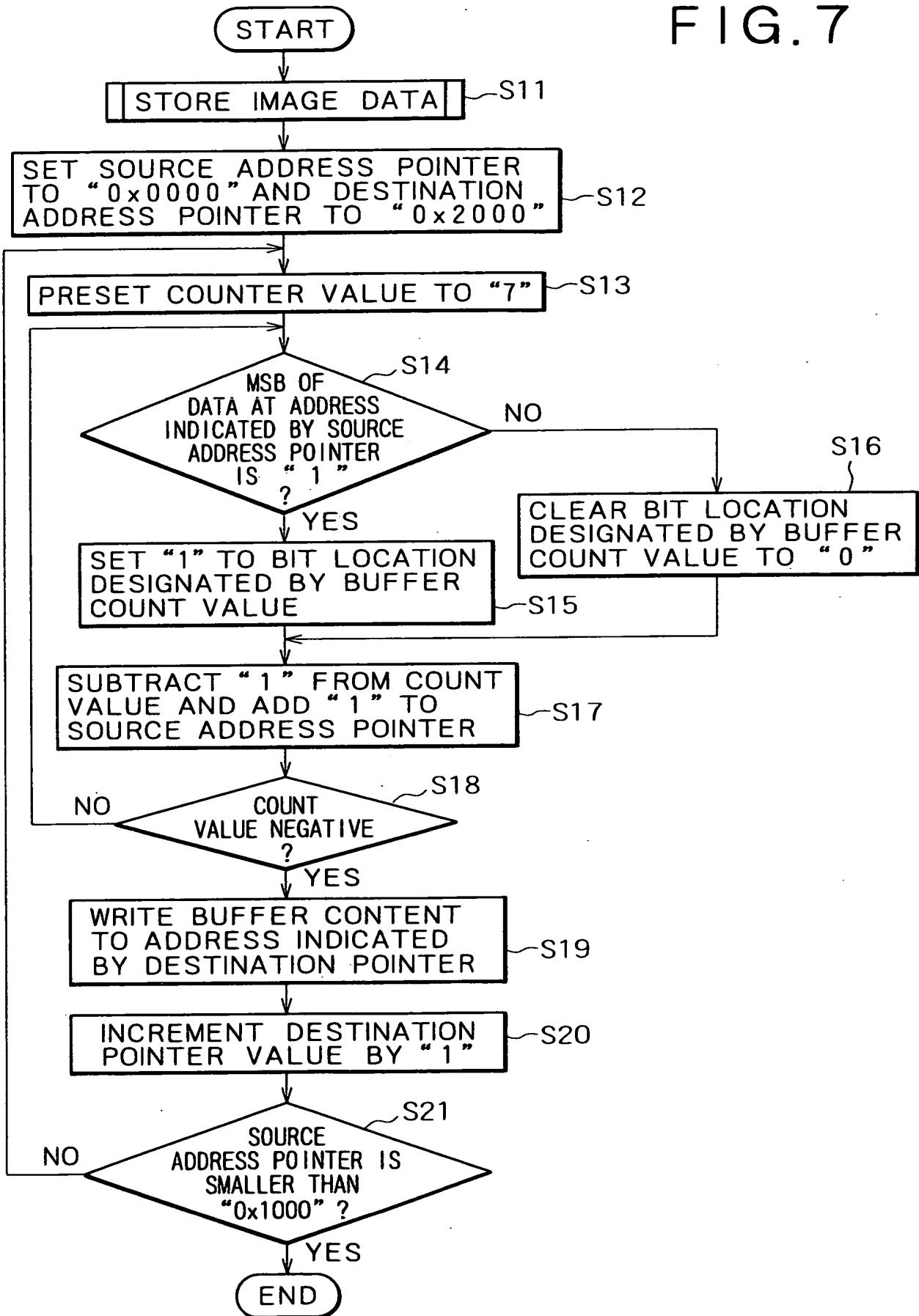


FIG. 6



TOEETZD "42450660

FIG. 7



10000000000000000000000000000000

F0E1C0 42450660

FIG. 8

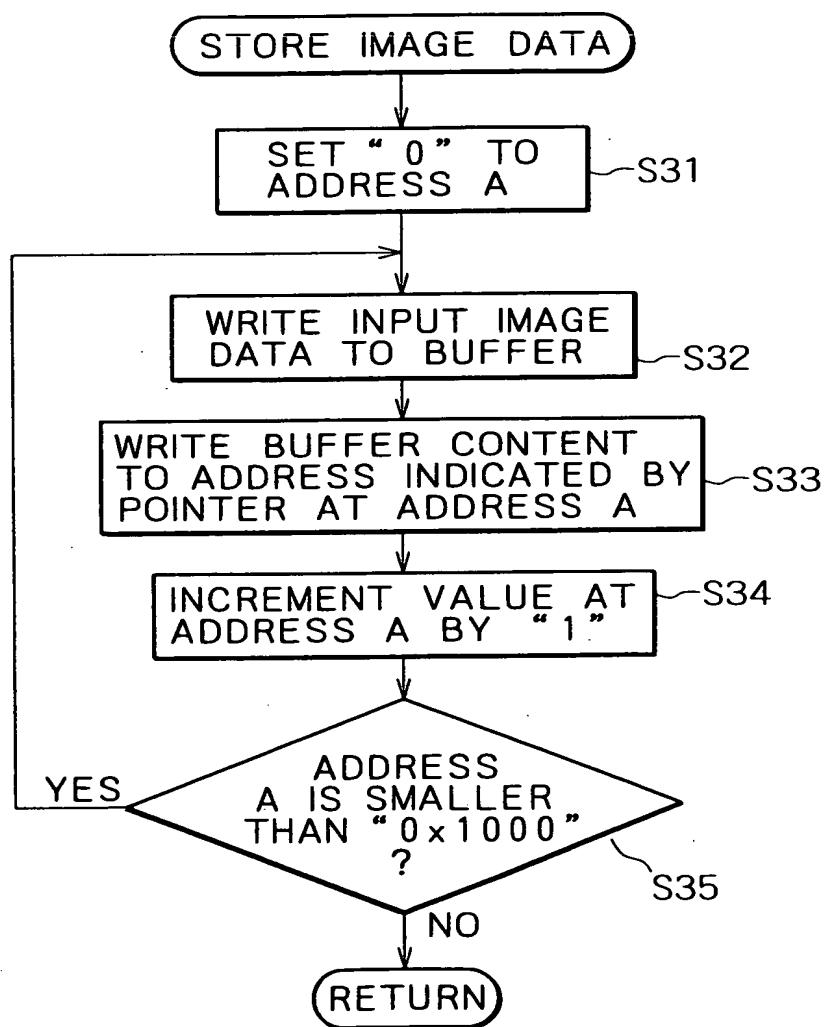


FIG. 9A

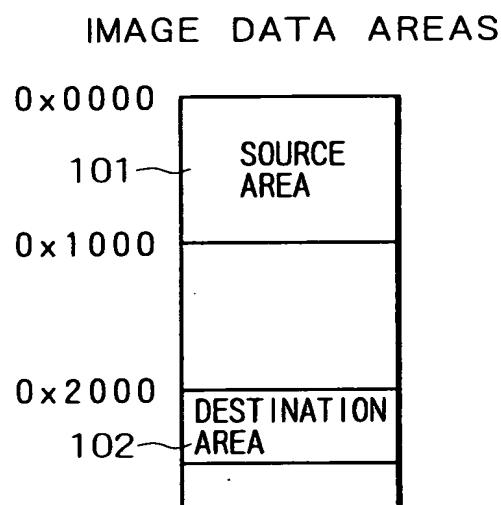


FIG. 9B

